Reduced-order Aggregate Model for Parallel-connected Single-phase Inverters

Victor Purba, Student Member, IEEE, Brian B. Johnson, Member, IEEE, Miguel Rodriguez, Saber Jafarpour, Member, IEEE, Francesco Bullo, Fellow, IEEE, and Sairaj V. Dhople, Member, IEEE

Abstract—This paper outlines a reduced-order aggregate dynamical model for parallel-connected single-phase grid-connected inverters. For each inverter, we place no restrictions on the converter topology and merely assume that the ac-side switch-averaged voltage can be controlled via pulse width modulation. The ac output of each inverter interfaces through an LCL filter to the grid. The closed-loop system contains a phase locked loop for grid synchronization, and real- and reactive-power control are realized with inner and outer PI current- and power-control loops. We derive a necessary and sufficient set of parametric relationships to ensure that a reduced-order aggregated state-space model for an arbitrary number of such paralleled inverters has the same model order and structure as any single inverter. We also present reduced-order models for the settings where the real- and reactive-power setpoints are different and where the inverters have different power ratings. We anticipate the proposed model being useful in analyzing the dynamics of large collections of parallel-connected inverters with minimal computational complexity. The aggregate model is validated against measurements obtained from a multi-inverter experimental setup consisting of three 750 VA paralleled grid-connected inverters, hence establishing robustness of the analytical result to parametric variations seen in practice.

Index Terms—Model reduction, phase-locked loop, single-phase inverter, voltage-source inverter.

I. INTRODUCTION

Rapid adoption of renewable sources of generation (e.g., photovoltaic (PV) energy conversion systems) and flexible loads (e.g., electric vehicles) has increased the number of power electronics inverters installed on the ac power grid. Scalable models that present limited computational burden will be critical to model and analyze the collective dynamics of large numbers of inverters in next-generation power networks [1]. To motivate the need for modeling strategies that can be applied to complex inverter systems, consider the relatively small island of Oahu which already has over 800,000 microinverters [2]. This number is expected to grow significantly as Hawaii aims to meet the goal of obtaining 100% of its energy from renewable sources [3]. Although the Hawaiian system is at the forefront of renewable adoption, it presents a glimpse at anticipated worldwide trends [4].

The disparity in ratings between individual inverters (no larger than a few MVA) and synchronous generators (several hundred MVA) implies that if the same net load were served with power electronics instead of generators, there would be an orders-of-magnitude increase in the number of energy-conversion interfaces (from a few thousand generators to potentially millions of inverters across a large-scale synchronous grid). Evaluating the stability and resilience of future power networks will therefore require accurate dynamical models for large collections of inverters that present limited computational burden. However, development of such models is challenged by the complexity of inverter dynamics (for instance, the particular model we examine in this paper is nonlinear, and composed of 16 states) and the sheer number of inverters that will eventually be commonplace on the ac power grid. To address the challenge of model complexity in multi-inverter systems, we propose an aggregate reduced-order state-space model for an arbitrary number of single-phase grid-tied inverters connected in parallel. While our analytical result is presented for identical inverters, we experimentally validate our findings which immediately establishes robustness to parametric variations that are likely to be seen in practice. We also present extensions of the main result on model reduction to cover cases when the power setpoints of the inverters are all different and the power ratings of the inverters are all different.

We examine the ac-timescale dynamics of a single-phase voltage source inverter (VSI) with an output $LCL$ filter. To ensure broad applicability across VSI topologies, we only assume that the switch-averaged voltage across the ac terminals is controllable via pulse width modulation and we neglect switch-level dynamics. The control architecture is composed of an inner current-control loop, an outer power-control loop, and a phase locked loop (PLL) for grid synchronization. This filter and control architecture are prototypical and it ensures broad applicability of the results. The state-space model that captures the dynamics of the inverter is composed of 16 states. The contributions of the paper are threefold:

1) For a parallel collection of $N$ inverters, we derive a necessary and sufficient set of parametric relationships for an aggregate reduced-order inverter model to have a state-space model with the same structure and model order (i.e., composed of the same 16 states referenced above) as any single inverter in the collection. (Figure 1

V. Purba and S. V. Dhople are with the Department of Electrical and Computer Engineering at the University of Minnesota, Minneapolis, MN (email: {purba002,sdhople}@umn.edu); B. B. Johnson is with the Department of Electrical and Computer Engineering at the University of Washington, Seattle, WA (e-mail: brianbj@uw.edu); M. Rodriguez was with the Power Systems Engineering Center at the National Renewable Energy Laboratory, Golden, CO (now with Advanced Micro Devices, Inc., Fort Collins, CO, e-mail: miguel.rodriguez@amd.com); S. Jafarpour and F. Bullo are with the Department of Mechanical Engineering at the University of California, Santa Barbara, Santa Barbara, CA (email: {saber.jafarpour, bullo}@engineering.ucsb.edu). This work was supported in part by the U.S. Department of Energy under Contract No. DE-EE0000-1583 and the National Science Foundation under grant 1453921.
illustrates the idea.)

2) We derive parameters for an aggregate reduced-order model (with the same order and structure as any individual inverter) for the case where the real and reactive-power setpoints for the inverters are all different.

3) We derive parameters for an aggregate reduced-order model (with the same order and structure as any individual inverter) for the case where the power ratings of the inverters are all different.

In general, the identical state-space model structure implies that from a topological vantage point, the aggregated equivalent model also maps to an inverter with an LCL filter, an inner current-control loop, an outer power-control loop, and a PLL for grid synchronization, except with different filter parameters and control gains. With reference to contribution 1) above, while some aspects of the aggregate model appear intuitive in hindsight (e.g., given the parallel arrangement, the output inductances in the aggregate model are \(1/N\) times those in any individual inverter), the parametric dependencies in pertinent control gains are not. What is more, the fact that the parametric relationships we derive in 1) are necessary and sufficient implies that we exhaustively enumerate all alternative possibilities to obtain a structurally similar reduced-order model. Contributions in 2) and 3) follow as corollaries to the main result in 1) and enable extending the result to obtain aggregate models under heterogeneous settings that are likely to be noticed in practice. Finally, we believe that the general state-space modeling formulation and the proof strategy can be extended to other control strategies to establish similar aggregate models.

A majority of prior literature pertaining to model reduction for energy conversion interfaces has understandably focused on the dynamics of fossil-fuel-driven synchronous generators [5]–[9]. More recently, there has been increased attention devoted to aggregate models for wind-turbines in utility-scale collector systems [10], electric vehicles [11], and demand response systems [12]. However, these are focused at a macro level that disregards the dynamics at faster time scales which arise from inverter filters and time-domain controllers. With regard to inverter dynamics, most of the related literature has predominantly focused on reduced-order models for individual grid-connected [13], islanded [14]–[16], and resonant inverters [17]. (Tangentially related is literature on model reduction of dc-dc converters [18]–[20] and induction machines [21], [22].) Model-reduction methods focused on collections of inverters have been limited to islanded settings [23], [24] and inverters with virtual-inertia emulation [25]. Both are application domains where inverters are controlled to emulate the dynamics of synchronous generators, and therefore, there is a natural translation of classical model-reduction methods for synchronous generators mentioned previously.

Given the landscape of related work discussed above, this work addresses a key gap in the literature pertaining to the dynamics of grid-connected multi-inverter systems. This paper significantly builds upon and extends our preliminary work in [26] where we developed similar aggregated models for parallel-connected three-phase inverters. Here, we examine the (admittedly different) filter and controller dynamics for single-phase inverters which will conceivably be more dominant in number in future distribution networks. As a further and important contribution, we provide experimental validation of our approach with a multi-inverter setup composed of three 750 VA grid-tied single-phase inverters. We note that the parallel aggregation approach proposed here provides the base for a broader set of aggregation techniques that can account for the network connection of the inverters. For instance, in [27] three-phase inverters are transferred to an auxiliary bus with the aid of auxiliary transformers, and subsequently aggregated using the parallel aggregation approach discussed here.

The remainder of this manuscript is organized as follows: In Section II, we establish mathematical notation and describe the grid-tied single-phase inverter model. The reduced-order model for a collection of these inverters connected in parallel is derived in Section III. We validate the model-reduction method by comparing numerical simulation results with results from the experimental prototype in Section IV. Finally, concluding remarks and directions for future work are in Section V.

II. PRELIMINARIES AND INVERTER DYNAMICAL MODEL

In this section, we first introduce mathematical notation used in the manuscript. Then we describe the single-phase inverter model, and develop a standard state-space model representation.

A. Notation

For a vector \(x \in \mathbb{R}^N\), \(\text{diag}(x) \in \mathbb{R}^{N \times N}\) returns a diagonal matrix with diagonal entries composed of entries of \(x\). All-ones and all-zeros vectors of length \(N\) are denoted by \(1_N\) and
0N, respectively. Finally, 0M×N denotes the all-zeros matrix of size M-by-N.

B. Dynamical Model of Single-phase Inverter

A block diagram of the grid-connected single-phase inverter is illustrated in Fig. 2. The PLL is designed to track the instantaneous angle of the grid voltage at the point of common coupling, the power controller regulates the real and reactive power delivered into the ac grid, and the current controller governs the current delivered by the switch terminals. This model represents a prototypical implementation in a single-phase grid-connected setting and captures all relevant ac-side system dynamics. Since the focus of the paper is at the ac-side point of common coupling, dynamics of the dc-link and any other converter stages that precede the dc-link are neglected. We briefly overview the reference-frame transformations and the dynamics of the filter and controllers next.

Reference-frame Transformations: The controllers illustrated in Fig. 2 are implemented in the dq domain. To enable this, the Hilbert transform [28] (denoted by \(G_{\pi/2}\)) is first utilized to generate orthogonal signals with quarter-cycle phase lag for each sinusoidal measurement, i.e., it yields signals in the \(\alpha\beta\) domain [29]. Each signal and its corresponding phase-shifted counterpart is subsequently processed by an \(\alpha\beta\) to dq transformation. The dq signals are then used in the PLL and current controller. Note that although the above formulation utilizes the Hilbert transform as a means of generating quarter-cycle phase-shifted waveforms, these signals can also be realized with a quarter-cycle delay buffer or an all-pass filter with appropriate phase response. The remainder of the manuscript focuses exclusively on the Hilbert transform without loss of generality.

The transfer function of the Hilbert transform is given by

\[
G_{\pi/2}(s) = \frac{\omega_{PLL} - s}{\omega_{PLL} + s},
\]

where \(\omega_{PLL}\) is the frequency returned by the PLL. As shown in Fig. 2, we will consider the measured signal to be the \(\alpha\)-component, and the corresponding output of the Hilbert transform as the \(\beta\) component. Next, signals in the \(\alpha\beta\) reference frame \((x^\alpha, x^\beta)\) are transformed to the dq reference-frame \((x^d, x^q)\) with the following rotation matrix [29]:

\[
\begin{bmatrix}
x^d \\
x^q
\end{bmatrix}
= \begin{bmatrix}
\cos \delta & \sin \delta \\
-\sin \delta & \cos \delta
\end{bmatrix}
\begin{bmatrix}
x^\alpha \\
x^\beta
\end{bmatrix},
\]

where \(\delta\) is the instantaneous angle generated by the PLL. As seen in Fig. 2, the PLL is in feedback with the dq transformation. The role of the PLL is to modulate the value of the PLL angle, \(\delta\), such that the d-axis component of the grid voltage, \(v^d_g\), is driven asymptotically to zero. From the definition of the \(\alpha\)- and \(\beta\)-components of \(v_g\) and the dq transformation in (2), it can be shown that if \(v^d_g = 0\), then \(\delta\) is the instantaneous phase angle of \(v_g\). (See Appendix A for a short derivation.)

Controller and Filter Dynamics: The internal controllers in the PLL comprise a low-pass filter with cut off frequency \(\omega_c,PLL\) and a PI controller with proportional and integral gains given by \(k_p^{PLL}\) and \(k_i^{PLL}\), respectively. The PLL dynamics are given by

\[
\begin{align}
\frac{d}{dt} v^{PLL} &= \omega_c,PLL (v^d_g - v^{PLL}), \\
\frac{d}{dt} \phi^{PLL} &= -v^{PLL}, \\
\frac{d}{dt} \delta &= \omega_{nom} - k_p^{PLL} v^{PLL} + k_i^{PLL} \phi^{PLL} =: \omega^{PLL}, \\
\frac{d}{dt} v^\beta_g &= \omega^{PLL} (v^\alpha_g - v^\beta_g) - \frac{d}{dt} v^d_g,
\end{align}
\]

where \(\omega_{nom}\) is the nominal grid frequency (e.g., \(2\pi \times 60\) or \(2\pi \times 50\) rad/s). We apply (1) to \(v_g\) to obtain the dynamics of \(v^\beta_g\) in (3d), and we apply (2) to \(v_g\) and \(v^\beta_g\) to obtain \(v^d_g\) which feeds into (3a). From above, we can see that \(v^d_g = v^{PLL} = 0\) in steady-state. Furthermore, when the grid frequency is \(\omega_{nom}\), it follows that \(\delta = \omega^{PLL} = \omega_{nom}\). Note that we assume the first derivative of \(v_g\), i.e., \(\frac{d}{dt} v_g\), to be well defined.

The LCL filter is composed of inverter-side inductance \(L_i\), grid-side inductance, \(L_g\), and filter capacitance, \(C_f\). The dynamics introduced by the LCL filter in the \(\alpha\beta\) frame are given by

\[
\begin{align}
\frac{d}{dt} i^\alpha_i &= \frac{1}{L_i} \left( -R_i i^\alpha_i + v^\alpha_i - v^\beta_i \right), \\
\frac{d}{dt} i^\beta_i &= \omega_{PLL} \left( i^\alpha_i - i^\beta_i \right) - \frac{d}{dt} i^\alpha_i, \\
\frac{d}{dt} i^\alpha_g &= \frac{1}{L_g} \left( -R_g i^\alpha_g + v^\alpha_g - v^\beta_g \right), \\
\frac{d}{dt} i^\beta_g &= \omega_{PLL} \left( i^\alpha_g - i^\beta_g \right) - \frac{d}{dt} i^\alpha_g, \\
\frac{d}{dt} v^\alpha_f &= R_f \left( \frac{d}{dt} i^\alpha_f - \frac{d}{dt} i^\alpha_g \right) + \frac{1}{C_f} (i^\alpha_f - i^\alpha_g), \\
\frac{d}{dt} v^\beta_f &= \omega_{PLL} \left( v^\alpha_f - v^\beta_f \right) - \frac{d}{dt} i^\beta_f,
\end{align}
\]

where the \(\alpha\)-component dynamics are derived from fundamental circuit laws, and the \(\beta\)-component expressions result from the application of (1) to the corresponding \(\alpha\)-component dynamics.

The power controller (PC) consists of two PI controllers with gains \(k_p^{PC}\) and \(k_i^{PC}\) and two low-pass filters with cut-off frequency \(\omega_{c,PC}\) (for the \(d\) and \(q\) components). The real- and reactive-power setpoints, \(p^*\) and \(q^*\), act as inputs to the power controller and its outputs are current references for the downstream current controller. These are generated as follows:

\[
\begin{align}
i^d_q^* &= k_p^{PC} (q^* - q_{avg}) + k_i^{PC} \int (q^* - q_{avg}), \\
i^d_p^* &= k_p^{PC} (p^* - p_{avg}) + k_i^{PC} \int (p^* - p_{avg}),
\end{align}
\]

where \(p_{avg}\) and \(q_{avg}\) are the outputs of the low-pass filters, with inputs to be the inverter real- and reactive-power outputs measured at the grid terminals, \(p\) and \(q\), respectively. In particular, with reference to Fig. 2 and with the aid of elementary trigonometric operations we have

\[
p = \frac{1}{2} (v^\alpha_g i^\alpha_g + v^\beta_g i^\beta_g), \quad q = \frac{1}{2} (v^\beta_g i^\alpha_g - v^\alpha_g i^\beta_g),
\]
The addition of the feedforward terms implies that the inverter terminal voltage follows the command reference perfectly and without delay.

C. State-space Representation of Inverter Dynamics

The dynamics of the LCL filter, PLL, power controller, and current controller for an individual inverter are now expressed in state-space form to facilitate analysis. To this end, corresponding to the power and current controllers, we will find it useful to introduce the auxiliary dynamics

\[
\begin{align*}
\frac{d}{dt} \phi^p &= p^* - p_{\text{avg}}, \\
\frac{d}{dt} \phi^q &= q^* - q_{\text{avg}}, \\
\frac{d}{dt} \gamma^d &= i^d_{\text{avg}} - i^d, \\
\frac{d}{dt} \gamma^q &= i^q_{\text{avg}} - i^q.
\end{align*}
\]

With these definitions in place, the dynamics (3a)–(11) can be represented in a compact state-space form

\[
\dot{x} = Ax + B_1 u_1 + B_2 u_2 + g(x, u_1, u_2),
\]

where the state vector, \( x \), and inputs \( u_1, u_2 \) are given by

\[
x = [i^d_1, i^d_2, i^q_1, i^q_2, i^{d*}_1, i^{d*}_2, q^{d*}_1, q^{d*}_2, p_{\text{avg}}, q_{\text{avg}}, \phi^p, \phi^q, v^p_{\text{PLL}}, v^q_{\text{PLL}}, \delta]^T,
\]

\[
u_1 = [p^*, q^*]^T, \quad u_2 = [v_k, \frac{d}{dt} i^d_k]^T.
\]

In order to show the entries of matrices \( A \in \mathbb{R}^{16 \times 16} \), \( B_1 \in \mathbb{R}^{16 \times 2} \), and \( B_2 \in \mathbb{R}^{16 \times 2} \), let us partition the state vector as \( x = [x_{\text{LCL}}^T, x_{\text{CC}}^T, x_{\text{PC}}^T, x_{\text{PLL}}^T]^T \), where \( x_{\text{LCL}} = [i^d_1, i^d_2, i^q_1, i^q_2, \omega^d_1, \omega^q_1]^T \), \( x_{\text{CC}} = [\gamma^d, \gamma^q]^T \), \( x_{\text{PC}} = [p_{\text{avg}}, q_{\text{avg}}, \phi^p, \phi^q]^T \), and \( x_{\text{PLL}} = [v^p_{\text{PLL}}, v^q_{\text{PLL}}, \delta]^T \). Then, we can write (12) as

\[
\begin{align*}
\dot{x}_{\text{LCL}} &= \begin{bmatrix} A_{\text{LCL}} & 0_{6 \times 2} & 0_{6 \times 4} & 0_{6 \times 4} \end{bmatrix} x_{\text{LCL}} \\
\dot{x}_{\text{CC}} &= \begin{bmatrix} 0_{2 \times 6} & 0_{2 \times 2} & A_{\text{CC}} & 0_{2 \times 4} \end{bmatrix} x_{\text{CC}} \\
\dot{x}_{\text{PC}} &= \begin{bmatrix} 0_{4 \times 6} & 0_{4 \times 2} & A_{\text{PC}} & 0_{4 \times 4} \end{bmatrix} x_{\text{PC}} \\
\dot{x}_{\text{PLL}} &= \begin{bmatrix} 0_{4 \times 6} & 0_{4 \times 2} & A_{\text{PLL}} & 0_{4 \times 4} \end{bmatrix} x_{\text{PLL}} + \begin{bmatrix} 0_{6 \times 2} \\
B_{\text{LCL}} \\
B_{\text{CC}} \\
B_{\text{PC}} \\
B_{\text{PLL}} \end{bmatrix} + \begin{bmatrix} u_1 \\
g(x, u_1, u_2) \end{bmatrix},
\end{align*}
\]

where entries of the nonzero sub-matrices \( A_{\text{LCL}}, A_{\text{CC}}, A_{\text{PC}}, A_{\text{PLL}}, B_{\text{CC}}, B_{\text{PC}}, B_{\text{LCL}}, B_{\text{PLL}} \), and the function \( g(x, u_1, u_2) : \mathbb{R}^{16} \times \mathbb{R}^2 \times \mathbb{R}^2 \rightarrow \mathbb{R}^{16} \) are spelled out in Appendix B.

III. AGGREGATION OF PARALLEL-CONNECTED INVERTERS

In this section, we first introduce parametric scalings required to realize the aggregate model for the parallel-connected inverters. Next, we prove that the aggregate model indeed captures all the scalings in pertinent states (currents, voltages, internal-control states) for the uniform setting as well as in cases with heterogeneous power setpoints and ratings.

A. Parametric scalings and structure of aggregate model

We consider \( N \) identical single-phase inverters (with model described in Section II) with the same setpoints, \( p^* \) and \( q^* \), connected in parallel to a grid bus. We are interested in an aggregated reduced-order model with the same structure and dimension as the model in (12):

\[
\dot{x} = A x + B_1 u_1 + B_2 u_2 + g(x, u_1, u_2).
\]

In particular: we desire matrices \( A^* \in \mathbb{R}^{16 \times 16}, B_1^* \in \mathbb{R}^{16 \times 2}, B_2^* \in \mathbb{R}^{16 \times 2} \), and function \( g^* : \mathbb{R}^{16} \times \mathbb{R}^2 \times \mathbb{R}^2 \rightarrow \mathbb{R}^{16} \).
have the same structure and dimension as $A, B_1, B_2,$ and $g$ in (12) (implying that the control architecture and output-filter arrangement of the aggregated model are the same as that in an individual inverter); entries of state vector $x^r$ and inputs $u_1^r, u_2^r$ to have the same connotation as states in $x$ and inputs $u_1, u_2$. In our main result, we demonstrate that if and only if the following relationships hold between parameters of the reduced-order and original models:

$$C_i^r = NC_i, \quad R_i^r = \frac{R_i}{N}, \quad L_i^r = \frac{L_i}{L}, \quad R_g^r = \frac{R_g}{N},$$

and also permute $x^r$ (corresponding to the reduced-order model (16)) the same way, denoting the permuted vector by $\hat{x}^r$. Denote $\hat{x}(t)$ to be the solution to the permuted version of (12) with initial condition $\hat{x}(0)$ and inputs $u_1, u_2$; and $\hat{x}^r(t)$ to be the solution to the permuted version of (16) with initial condition $\hat{x}^r(0)$ and inputs $u_1^r, u_2^r$. Suppose initial conditions are such that $\hat{x}^r(t_0) = \text{diag}(\Psi)\hat{x}(t_0)$, where the scaling vector, $\Psi := [N 1_0, 1_0^T]^T$, and the inputs are: $u_1^r = Nu_1, u_2^r = u_2$ (see (14)). The states of the reduced-order model and the individual inverter are related as:

$$\hat{x}^r(t) = \text{diag}(\Psi)\hat{x}(t), \quad \forall t \geq t_0, \quad (19)$$

and if only if their parameters are related as (17a)–(17b).

In particular, given the definition of the scaling vector, $\Psi$, (19) establishes the following relationships between states of the reduced-order model and those in the individual inverter model $\forall t \geq t_0$:

$$[\hat{\alpha}_i \hat{\beta}_i, \hat{\gamma}_i \hat{\delta}_i, \gamma_i, \hat{p}_{avg}, \hat{q}_{avg}, \hat{p}_{avg}^r, \hat{q}_{avg}^r]^T = N[\alpha_i, \beta_i, \gamma_i, \delta_i, \gamma_i, \hat{p}_{avg}, \hat{q}_{avg}, \hat{p}_{avg}^r, \hat{q}_{avg}^r]^T, \quad (20a)$$

$$[\hat{\alpha}^r_i \hat{\beta}^r_i, \hat{\gamma}^r_i \hat{\delta}^r_i, \hat{\gamma}_{avg}^r, \hat{\delta}_{avg}^r]^T = [\alpha_i, \beta_i, \gamma_i, \delta_i, \gamma_i, \hat{p}_{avg}, \hat{q}_{avg}, \hat{p}_{avg}^r, \hat{q}_{avg}^r]^T. \quad (20b)$$

Proof. Let us define $z := \hat{x}^r - \text{diag}(\Psi)\hat{x}$. The dynamics of $z$ are given by

$$\dot{z} = \hat{x}^r - \text{diag}(\Psi)\hat{x} = \hat{A}^r \hat{x}^r + \hat{B}_1^r u_1^r + \hat{B}_2^r u_2^r + \hat{g}(\hat{x}^r, u_1^r, u_2^r) - \text{diag}(\Psi)\hat{A}\hat{x} - \text{diag}(\Psi)\hat{B}_1 u_1 - \text{diag}(\Psi)\hat{B}_2 u_2 - \text{diag}(\Psi)\hat{g}(\hat{x}, u_1, u_2), \quad (21)$$

where matrices $\hat{A}, \hat{B}_1, \hat{B}_2, \hat{A}^r, \hat{B}_1^r, \hat{B}_2^r$ and functions $\hat{g}(\hat{x}, u_1, u_2)$, $\hat{g}(\hat{x}^r, u_1^r, u_2^r)$ are appropriately permuted versions of corresponding matrices and functions in (12) and (16). We will now show that $z = 0, \forall t \geq t_0$ when $z(t_0) = \hat{x}^r(t_0) - \text{diag}(\Psi)\hat{x}(t_0) = 0$. This would further imply that $z(t) = \hat{x}^r(t) - \text{diag}(\Psi)\hat{x}(t) = 0_{16}, \forall t \geq t_0$, as claimed in the statement of the Theorem.

Partition $\hat{x} = [\hat{x}_1^T, \hat{x}_2^T]^T$, where $\hat{x}_1 = [\hat{\alpha}_i \hat{\beta}_i, \hat{\gamma}_i \hat{\delta}_i, \gamma_i, \hat{p}_{avg}, \hat{q}_{avg}, \hat{p}_{avg}^r, \hat{q}_{avg}^r]^T$ and $\hat{x}_2 = [\hat{\alpha}^r_i \hat{\beta}^r_i, \hat{\gamma}^r_i \hat{\delta}^r_i, \hat{\gamma}_{avg}^r, \hat{\delta}_{avg}^r]^T$, and we also partition $\hat{x}^r$ the same way. Then, we partition the appropriately permuted versions of (12) and (16) as

$$\begin{bmatrix}
\hat{x}_1 \\
\hat{x}_2
\end{bmatrix} =
\begin{bmatrix}
\hat{A}_{11} & \hat{A}_{12} \\
\hat{A}_{21} & \hat{A}_{22}
\end{bmatrix}
\begin{bmatrix}
\hat{x}_1 \\
\hat{x}_2
\end{bmatrix} +
\begin{bmatrix}
\hat{B}_{11} \\
\hat{B}_{21}
\end{bmatrix} u_1 +
\begin{bmatrix}
\hat{B}_{22} \\
\hat{B}_{22}
\end{bmatrix} u_2$$

Figure 3: (a) Reduced-order aggregate single-phase inverter model and adopted shorthand representation. The inverter-side inductance and resistance are related to those in the individual model as $\frac{L_i}{L} = \frac{R_i}{R}$. (b) Dynamics of the parallel connection of $N$ single-phase inverters can be captured by the aggregate inverter model in Fig. 3a.
From the definition of matrices $A_{LCL}$, $A_{CC}$, $A_{PC}$, $A_{PLL}$, $B_{CC}$, $B_{PC}$, $B_{LCL}$, $B_{PLL}$ in Appendix B, and the parametric scalings established in (17a)–(17b), we note the following:

$$A'_{11} = A_{11}, \quad A'_{12} = N A_{12}, \quad A'_{21} = \frac{1}{N} A_{21}, \quad A'_{22} = A_{22},$$

$$B'_{11} = B_{11}, \quad B'_{12} = \frac{1}{N} B_{12}, \quad B'_{21} = N B_{21}, \quad B'_{22} = B_{22}.$$  

Then, we have

$$\text{diag}(\Psi) \hat{A} = \begin{bmatrix} A'_{11} & N A'_{12} \\ A_{21} & A_{22} \end{bmatrix} = \begin{bmatrix} N A_{11} & N A_{12} \\ A_2 & A_2 \end{bmatrix} = \begin{bmatrix} N \hat{A}_{11} & N \hat{A}_{12} \\ N \hat{A}_{21} & N \hat{A}_{22} \end{bmatrix},$$

$$\text{diag}(\Psi) \hat{B}_1 = \begin{bmatrix} N \hat{B}_{11} \\ \hat{B}_{12} \end{bmatrix} = \begin{bmatrix} N \hat{B}_{11} \\ \hat{B}_{12} \end{bmatrix} = N \hat{B}_1,$$

$$\text{diag}(\Psi) \hat{B}_2 = \begin{bmatrix} \hat{B}_{21} \\ \hat{B}_{22} \end{bmatrix} = \hat{B}_2.$$  

The next step is to show that $\hat{g}'(\text{diag}(\Psi) \hat{x}, u_1', u_2') = \text{diag}(\Psi) \hat{g}(\hat{x}, u_1, u_2)$. Let $\hat{g}(\hat{x})$ and $\hat{g}'(\text{diag}(\Psi) \hat{x})$ denote the $\ell$-th entry of $\hat{g}(\hat{x}, u_1, u_2)$ and $\hat{g}'(\text{diag}(\Psi) \hat{x}, u_1', u_2')$, respectively. The nonzero entries of $\hat{g}'(\text{diag}(\Psi) \hat{x}, u_1', u_2')$ are related to the corresponding entries of $\hat{g}(\hat{x}, u_1, u_2)$ through:

$$\hat{g}_{11}(\text{diag}(\Psi) \hat{x}) = \frac{N}{I_1} \left[ \frac{k p_{PC}(N q^* - N q_{avg})}{N} + k_{PC} N q^* - N q_{avg} \right] \cos \delta - \left( \frac{N}{I_2} \frac{k_{PC}(N p^* - N p_{avg})}{N} \right) \sin \delta = N \hat{g}_1(\hat{x}),$$

$$\hat{g}_{12}(\text{diag}(\Psi) \hat{x}) = \eta (N i_1^d - N i_1^q),$$

$$\hat{g}_{21}(\text{diag}(\Psi) \hat{x}) = \eta (N i_2^d - N i_2^q),$$

$$\hat{g}_{22}(\text{diag}(\Psi) \hat{x}) = \frac{\omega_{PC}}{2} (v_{g}^* N i_1^q + v_{g}^* N i_1^q) = N \hat{g}_2(\hat{x}),$$

$$\hat{g}_{11}(\text{diag}(\Psi) \hat{x}) = R_{l} \hat{g}_1(\text{diag}(\Psi) \hat{x}) = R l \hat{g}_1(\hat{x}) = \hat{g}_{11}(\hat{x}),$$

$$\hat{g}_{12}(\text{diag}(\Psi) \hat{x}) = \eta (v_{g}^* - v_{g}^*),$$

$$\hat{g}_{21}(\text{diag}(\Psi) \hat{x}) = \eta (v_{g}^* - v_{g}^*),$$

$$\hat{g}_{14}(\text{diag}(\Psi) \hat{x}) = \omega_{PLL}(v_{g}^* \cos \delta + v_{g}^* \sin \delta) = \hat{g}_{14}(\hat{x}),$$

$$\hat{g}_{16}(\text{diag}(\Psi) \hat{x}) = \omega_{om} = \hat{g}_{16}(\hat{x}),$$

with $\eta := -k_{PLL} v_{PLL} + k_{ PLL} \Phi_{PLL}$. Therefore we have

$$\text{diag}(\Psi) \hat{g}(\hat{x}, u_1, u_2) = \hat{g}'(\text{diag}(\Psi) \hat{x}, u_1', u_2').$$  

Notice that the PLL dynamics ((3a)–(3d)) are decoupled from the remainder of the states in the state-space model. Therefore, the parameters of the PLL in the individual and reduced-order models are the same, and we can conclude that:

$$v_{g}^* = v_{g}^*, \quad v_{PLL}^* = v_{PLL}^*, \quad \Phi_{PLL} = \Phi_{PLL}, \quad \delta = \delta.$$  

Now, consider the function $h(\hat{x}, u_1', u_2') : \mathbb{R}^6 \times \mathbb{R}^2 \to \mathbb{R}^6$, which is defined to have the same structure as $\hat{g}'(\text{diag}(\Psi) \hat{x}, u_1', u_2')$ except that its 13th, 14th, and 16th entries are 0. (See Appendix B for details.) Then, the following holds

$$\hat{g}'(\text{diag}(\Psi) \hat{x}, u_1', u_2') = h((\hat{x} - \text{diag}(\Psi) \hat{x}), 0, u_2').$$  

Using identities (25)–(30) in (21), we have

$$\hat{z} = \hat{A} (\hat{x} - \text{diag}(\Psi) \hat{x}) + h((\hat{x} - \text{diag}(\Psi) \hat{x}), 0, u_2') = \hat{A} \hat{z} + h(z, 0, u_2').$$

If we initialize $z(t_0) = 0_{16}$, we have $z(t) = 0_{16}, \forall t \geq t_0$, due to the fact that $h(0_{16}, 0, u_2) = 0_{16}$. By the definition of $z(t)$, we have $z(t) = (\text{diag}(\Psi) \hat{x}(t), \forall t \geq t_0$.

For the other direction, given that $z(t) = \hat{x}^* - \text{diag}(\Psi) \hat{x}, \forall t \geq t_0, u_1' = N u_1$, and $u_2' = u_2$, (21) can be written as

$$0_{15} = (\hat{A} \text{diag}(\Psi) - \text{diag}(\Psi) \hat{A}) \hat{x} + (N \hat{B}_1 - \text{diag}(\Psi) \hat{B}_1) u_1 + (\hat{B}_2 - \text{diag}(\Psi) \hat{B}_2) u_2 + \hat{g}'(\text{diag}(\Psi) \hat{x}, N u_1, u_2) - \text{diag}(\Psi) \hat{g}(\hat{x}, u_1, u_2).$$  

The equality above is satisfied when the following identities hold:

$$\hat{A} \text{diag}(\Psi) = \text{diag}(\Psi) \hat{A}, \quad N \hat{B}_1 = \text{diag}(\Psi) \hat{B}_1,$$

$$\text{diag}(\Psi) \hat{B}_2 = \text{diag}(\Psi) \hat{B}_2.$$

It emerges that $R_l$, $L_i$, $k_{PC}$, and $k_{PLL}$ always appear in the identities above as fractions: $R_{l} \text{,} k_{PC} N$, and $k_{PLL} N$. Therefore, these parameters relate to those in the reduced-order model through (17b). The remainder of the parameters can be determined straightforwardly from (33) and (34); they are given uniquely by (17a), including the unchanged parameters (i.e., those which are not mentioned in (17a) and (17b)). This concludes the proof.

C. Corollaries for Heterogeneous Settings

We now present two corollaries. In the first, we examine inverters with different reference-power setpoints for both active and reactive power. In this particular case, the relationships between the states of the reduced-order model and those of the individual inverters $\ell = 1, \ldots, N$ are as follows $\forall t \geq t_0$:

$$\begin{bmatrix} v_{i_1^r, \ldots, i_1^r} \\ v_{i_1^d, \ldots, i_1^d} \\ i_{g_1^r, \ldots, i_{g_1^r}} \\ i_{g_1^d, \ldots, i_{g_1^d}} \end{bmatrix} T = \begin{bmatrix} v_{i_1^r, \ldots, i_1^r} \\ v_{i_1^d, \ldots, i_1^d} \\ i_{g_1^r, \ldots, i_{g_1^r}} \\ i_{g_1^d, \ldots, i_{g_1^d}} \end{bmatrix} T$$

$$= \sum_{\ell=1}^{N} \begin{bmatrix} i_{g_1^r, \ldots, i_{g_1^r}} \\ i_{g_1^d, \ldots, i_{g_1^d}} \end{bmatrix} T = \begin{bmatrix} v_{g_1^r, \ldots, v_{g_1^r}} \\ v_{g_1^d, \ldots, v_{g_1^d}} \end{bmatrix} T,$$

$$\begin{bmatrix} i_{g_1^r, \ldots, i_{g_1^r}} \end{bmatrix} T = \begin{bmatrix} v_{g_1^r, \ldots, v_{g_1^r}} \end{bmatrix} T,$$

$$\begin{bmatrix} v_{g_1^r, \ldots, v_{g_1^r}} \end{bmatrix} T = \begin{bmatrix} v_{g_1^r, \ldots, v_{g_1^r}} \end{bmatrix} T$$

$$\forall \ell.$$
This model is useful in, e.g., PV systems where the incident irradiation might be different for different inverters (hence resulting in different values for \( p^* \)) and where local-voltage control may be implemented by modulating reactive-power injections (hence resulting in different values of \( q^* \)).

In the second corollary, we examine inverters with different power ratings, and derive an aggregate model with currents that scale systematically. To formalize this, we define a power-scaling parameter \( \kappa_\ell \) for the \( \ell \)th inverter as [26]:

\[
\kappa_\ell = \frac{P_{\text{rated,}\ell}}{P_{\text{base}}} = \frac{1}{k_\ell},
\]

where \( P_{\text{rated,}\ell} \) and \( P_{\text{base}} \) denote the rated power of the \( \ell \)th inverter in the parallel system and system-wide base value, respectively. Without loss of generality, we assume that the inverter model in Fig. 2 has a power rating equal to the base value. We also introduce the notion of an equivalent power-scaling parameter:

\[
\bar{\kappa} := \sum_{\ell=1}^{N} \kappa_\ell.
\]

The states of the reduced-order model relate to those in the individual inverters \( \ell = 1, \ldots, N \) and the unscaled inverter (i.e., inverter model with rating equal to the base value) as follows \( \forall \ell \geq t_0 \):

\[
\begin{align*}
    [\bar{v}^r_{\ell,t}, \delta^r_{\ell,t}]^T &= \sum_{\ell=1}^{N} [v^r_{\ell,t}, \delta^r_{\ell,t}]^T, \\
    [\bar{v}^u_{\ell,t}, \bar{v}^f_{\ell,t}, \bar{v}^p_{\ell,t}, \bar{v}^{PLL}_{\ell,t}, \bar{v}^{qPLL}_{\ell,t}, \bar{v}^{qPLL}_{\ell,t}]^T &= \sum_{\ell=1}^{N} [v^u_{\ell,t}, v^f_{\ell,t}, v^p_{\ell,t}, v^{PLL}_{\ell,t}, v^{qPLL}_{\ell,t}, v^{qPLL}_{\ell,t}]^T,
\end{align*}
\]

Formal results establishing these two aspects follow next.

**Corollary 1. (Aggregation of parallel-connected identical single-phase inverters with different reference-power setpoints)** Let us denote \( x_\ell, p^*_\ell \), and \( q^*_\ell \) as the state vector, real-, and reactive-power setpoints of the \( \ell \)th inverter in the parallel system. Permute \( x_\ell \) the same way as in (18), denoting the permuted vector as \( \tilde{x}_\ell \). Partition \( \tilde{x}_\ell = [\lambda_\ell^T, v_\ell^{\alpha,\beta,\gamma,T}, x_\text{PLL}_\ell]^T \), where

\[
\begin{align*}
    \lambda_\ell &= [v_{\ell,1}^{\alpha,\beta,\gamma}, v_{\ell,2}^{\alpha,\beta,\gamma}, \ldots, v_{\ell,\ell}^{\alpha,\beta,\gamma}]^T, \\
    v_{\ell,1}^{\alpha,\beta,\gamma} &= [v_{\ell,1}^{\alpha,\beta,\gamma}]^T, \quad \text{and} \quad x_\text{PLL}_\ell = [x_{\ell,1}^{\vee,\gamma,\psi}, x_{\ell,\ell}^{\vee,\gamma,\psi}]^T.
\end{align*}
\]

We also permute and partition \( x^\ell \) the same way, denoting the permuted vector as \( \tilde{x}^\ell = [\lambda^\ell^T, v^\ell^{\alpha,\beta,\gamma,T}, x_\text{PLL}^\ell]^T \). Suppose the initial conditions are such that \( \lambda^\ell(t_0) = \sum_{\ell=1}^{N} \lambda_\ell(t_0), v_{\ell,1}^{\alpha,\beta,\gamma}(t_0) = \frac{1}{N} \sum_{\ell=1}^{N} v_{\ell,1}^{\alpha,\beta,\gamma}(t_0), \) and \( x_\text{PLL}^\ell(t_0) = x_{\ell,\ell}^{\vee,\gamma,\psi}(t_0), \forall \ell \), and the inputs are:

\[
\begin{align*}
    u_1^\ell &= \sum_{\ell=1}^{N} u_1^\ell, \quad u_2^\ell = u_2, \forall \ell.
\end{align*}
\]

It follows that, \( \forall \ell \geq t_0 \):

\[
\begin{align*}
    \lambda^\ell(t) &= \sum_{\ell=1}^{N} \lambda_\ell(t), \quad v_{\ell,1}^{\alpha,\beta,\gamma}(t) = \frac{1}{N} \sum_{\ell=1}^{N} v_{\ell,1}^{\alpha,\beta,\gamma}(t), \quad x_\text{PLL}^\ell(t) = x_{\ell,\ell}^{\vee,\gamma,\psi}(t), \forall \ell,
\end{align*}
\]

if and only if the parameters of the reduced-order are related to the individual inverters through (17a)–(17b).

**Proof.** The proof is provided in Appendix C.

**Corollary 2. (Aggregation of parallel-connected single-phase inverters with heterogeneous power ratings)** The parameters of each inverter are related to the unscaled inverter through

\[
\begin{align*}
    C_{\ell,\ell} &= \kappa_\ell C_{\ell,1}, R_{\ell,\ell} = \frac{R_\ell}{\kappa_\ell}, L_{\ell,\ell} = \frac{L_\ell}{\kappa_\ell}, R_{\ell,\ell} = \frac{R_\ell}{\kappa_\ell}, \quad (41a) \\
    R_{\ell,\ell} &= \frac{R_\ell}{\kappa_\ell}, L_{\ell,\ell} = \frac{k^p_{CC,\ell}}{L_{\ell,\ell}}, R_{\ell,\ell} = \frac{k^q_{CC,\ell}}{L_{\ell,\ell}}, \quad (41b)
\end{align*}
\]

and parameters not mentioned are unchanged. Suppose the reference-power setpoints for each inverter are \( p^*_\ell = \kappa_\ell p^* \) and \( q^*_\ell = \kappa_\ell q^* \). The parameters of the reduced-order model are related to the unscaled inverter through

\[
\begin{align*}
    C^\ell &= \kappa C_{\ell,1}, R^\ell = \frac{R_\ell}{\kappa}, L^\ell = \frac{L_\ell}{\kappa}, R^\ell = \frac{R_\ell}{\kappa}, \quad (42a) \\
    R^\ell &= \frac{R^\ell}{\kappa}, L^\ell = \frac{k^p_{CC}}{L^\ell}, R^\ell = \frac{k^q_{CC}}{L^\ell}, \quad (42b)
\end{align*}
\]

Parameters not mentioned are unchanged. Let \( x_\ell, x_t, x^\ell \) denote the state vectors of the unscaled inverter model, \( \ell \)th inverter of the parallel system, and the reduced-order model, respectively. Permute the state vectors the same way as in (18), denoting the permuted vectors as \( \tilde{x}_\ell, \tilde{x}_t, \tilde{x}^\ell \). Partition the permuted state vector \( \tilde{x} = [\lambda^T, v^T]^T \), where \( \lambda = [i^\alpha, i^\beta, i^\gamma, j^\alpha, j^\beta, j^\gamma, i_{\text{PLL}}, j_{\text{PLL}}, \delta]^T \), and \( v = [v^1, v^2, v^3, v_{\text{PLL}}^\ell, v_{\text{PLL}}^\ell, \delta]^T \). We also partition \( \tilde{x}_\ell \) and \( \tilde{x}^\ell \) the same way: \( \tilde{x}_\ell = [\lambda_\ell^T, v_\ell^T]^T, \tilde{x}^\ell = [\lambda^T, v^\ell]^T \). Suppose the initial conditions are such that \( \lambda^\ell(t_0) = \sum_{\ell=1}^{N} \lambda_\ell(t_0) = \kappa \lambda(t_0), v^\ell(t_0) = v(t_0), \forall \ell, \) and the inputs are:

\[
\begin{align*}
    u_1^\ell &= \sum_{\ell=1}^{N} u_1, \quad u_2^\ell = u_2, \quad u^2 = u_2, \forall \ell.
\end{align*}
\]

It follows that for \( t \geq t_0 \):

\[
\begin{align*}
    \lambda^\ell(t) &= \sum_{\ell=1}^{N} \lambda_\ell(t) = \kappa \lambda(t), \quad v^\ell(t) = v(t) = v(t), \forall \ell, \quad (44)
\end{align*}
\]

if and only if the parameters of the reduced-order model are related to the unscaled inverter through (42a)–(42b).

**Proof.** Each of the inverters in the parallel system can be viewed as the aggregate of \( \kappa_\ell \) inverters, while keeping in mind that \( \kappa_\ell \) is not necessarily an integer. The rest of this proof is straightforward from Theorem 1.

**IV. EXPERIMENTAL VALIDATION & SIMULATION RESULTS**

In this section, we outline results from an experimental prototype and an exhaustive simulation study to demonstrate various aspects of the reduced-order model. The purpose and scope of the experiments is to demonstrate the validity and establish the accuracy of the reduced-order model (under uniform and symmetric settings) and this is done by comparing the net current injected by the parallel system of inverters in hardware to the output current of the aggregated reduced-order model. The experiments also establish robustness of the...
reduced-order model to parametric variations that are indeed inescapable in any hardware setup. Following the experimental results, we also include an exhaustive simulation study that: validates the reduced-order model derived for heterogeneous settings (Corollaries 1 and 2), investigates robustness of the reduced-order model to variations in filter parameters, and demonstrates the computational benefits of the reduced-order model.

### A. Hardware Setup

To validate the reduced-order model, we built an experimental system comprised of three identical 750 VA single-phase inverters connected in parallel across a stiff voltage source. The hardware setup is illustrated in Fig. 4(a). It consists of three distinct inverters each with a dedicated power stage and a TI F28335 DSP controller. Each inverter utilizes the control structure shown in Fig. 2. Controllers are discretized with a step size of $1/(15 \times 10^5)$ s and unipolar sine-triangle PWM is utilized with a switching frequency of 30 kHz. The single-phase 60 Hz, 120 V RMS ac system voltage (i.e., the grid point of interconnection) is realized with an Ametek MX-45 grid simulator. Subsequently, measurements obtained from the multi-inverter system are compared to a software simulation of a simple aggregated inverter model (see Figs. 3a and 4(b)). The simulation of the aggregated inverter, as given in Fig. 4(b), was carried in MATLAB with the ODE45 solver, performed on a computer with Intel Core i7-7700HQ processor @ 2.80GHz CPU and 8GB RAM. The parameters of the experimental setup are summarized in Table I. Simulation parameters used in the aggregated inverter model are obtained from these, and the scalings reported in (17a)–(17b).

### B. Validation of Reduced-order Model

To validate the proposed reduced-order model, we compared the measured and simulated dynamic responses under a comprehensive set of step changes in both real and reactive power. The real-power steps are representative of, e.g., sudden irradiance transients that a microinverter system might contend with. The reactive-power steps are representative of, e.g., ancillary services that grid-connected inverters may provide. Results are plotted in Fig. 5. The plot pair in each subfigure illustrates:

1) The measured net sinusoidal current injected into the grid by the parallel inverters overlaid with the simulated current from the aggregated-inverter model. The measurement point and corresponding point in the reduced-order model are marked prominently in Fig. 4.

### C. Simulation Study

Next, we establish the accuracy and computational benefits of the proposed reduced-order model (for a system of 100 parallel-connected inverters) in heterogeneous settings with numerical simulation results. The parameters of the inverter with nominal power rating are listed in Table I. We consider the following cases: #1) Inverters have heterogeneous power ratings with power-scaling parameters $\kappa$ selected to be uniformly distributed between 0.5 and 5. #2) All inverters have ratings that match the nominal power ratings, but their LCL-filter parameters vary between $\pm10\%$ of their nominal values. #3) Same setup as #2, but the LCL-filter parameters of the inverters vary between $\pm80\%$ of their nominal values. For all cases, the real- and reactive-power setpoints of the inverters are assumed to be uniformly distributed between 0 − 200 W and 0 − 100 VAR, respectively, and we perform a step change.
Figure 5: Comparison of experimentally measured and simulated waveforms: (a) Real-power step up \( p^* : 30 \text{ W} \to 600 \text{ W} \) with fixed \( q^* = 0 \text{ VAR} \), (b) Real-power step down \( p^* : 700 \text{ W} \to 50 \text{ W} \) with fixed \( q^* = 0 \text{ VAR} \), (c) Reactive-power step up \( q^* : 0 \text{ VAR} \to 500 \text{ W} \) with fixed \( p^* = 200 \text{ W} \), (d) Reactive-power step down \( q^* : 500 \text{ VAR} \to 0 \text{ W} \) with fixed \( p^* = 250 \text{ W} \).

A. Steady-state Operation of PLL

Express the grid voltage as \( v_g = V_g \sin(\delta_g) \), where \( V_g \) and \( \delta_g \) are the voltage amplitude and angle, respectively. The corresponding \( \alpha \beta \) components of \( v_g \) are given by

\[
\begin{align*}
v_g^\alpha &= v_g = V_g \sin(\delta_g), \\
v_g^\beta &= V_g \sin \left( \delta_g - \frac{\pi}{2} \right) = -V_g \cos(\delta_g).
\end{align*}
\]

The \( d \)-axis component of \( v_g \) is obtained from (2) as

\[
v_g^d = V_g \cos(\delta_g) - V_g \sin(\delta_g) \cos(\delta_g) = V_g \sin(\delta_g - \delta).
\]

From above, it follows that when \( \delta = \delta_g \), \( v_g^d = 0 \).

V. Concluding Remarks and Directions for Future Work

In this paper, we derived a reduced-order aggregated model for identical parallel-connected grid-tied single-phase inverters and extensions covering cases when the inverter power-setpoints are different and the inverter power ratings are different. The reduced-order model preserves the structure and has the same order as any individual inverter in the parallel collection. Experimental validation was provided to establish the accuracy of the reduced-order model in capturing ac-side dynamics of inverters during large-signal transients, and simulation results were provided to demonstrate computational benefits and robustness to parametric variations. Directions for future work include analytically establishing error bounds on the trajectories returned by reduced-order models in the face of parametric variations in the filter and control parameters.
$$A_{CC} = \begin{bmatrix} 0 & -k_{PC}^P & 0 \\ -k_{PC}^P & 0 & 0 \\ 0 & 0 & k_{PC}^P \\ \end{bmatrix},$$

$$A_{PC} = \begin{bmatrix} -\omega_{c,PC} & 0 & 0 & 0 \\ 0 & -\omega_{c,PC} & 0 & 0 \\ 0 & 0 & -1 & 0 \\ \end{bmatrix},$$

$$B_{CC} = \begin{bmatrix} 0 \\ k_{PC}^P \\ 0 \\ \end{bmatrix},$$

$$B_{PC} = \begin{bmatrix} 0 \\ 0 \\ -1 \\ \end{bmatrix}.$$

Lastly, the entries of $g(x, u_1, u_2)$, with $g_\ell$ denotes the $\ell$-th entry of $g(x, u_1, u_2)$, are

$$g_1 = \frac{1}{L_i} \left( k_{CC}^P (i_1^{d*} - i_1^d \cos \delta - i_1^d \sin \delta) + k_{CC}^P \gamma d \right) \cos \delta,$$

$$g_2 = \eta (v_i^{\alpha} - v_1^\alpha) - g_1, \quad g_3 = 0, \quad g_4 = 0,$$

$$g_5 = R_i g_1, \quad g_6 = \eta (v_i^{\alpha} - v_1^\alpha) - g_5,$$

$$g_7 = -i_1^c \cos \delta - i_1^c \sin \delta, \quad g_8 = i_1^c \sin \delta - i_1^c \cos \delta,$$

$$g_9 = \frac{\omega_{c,PLL}}{2} (v_i^{\alpha} + v_1^{\alpha} \gamma), \quad g_{10} = \frac{\omega_{c,PLL}}{2} (v_i^{\alpha} - v_1^{\alpha} \gamma),$$

$$g_{11} = 0, \quad g_{12} = 0, \quad g_{13} = \eta (v_i^{\alpha} - v_1^\alpha),$$

$$g_{14} = \omega_{c,PLL} (v_i^{\alpha} + v_1^{\alpha} \gamma), \quad g_{15} = 0, \quad g_{16} = \omega_{nom},$$

Where $\eta := -k_{PLL} v_{PLL} + k_{PLL}^\alpha \phi_{PLL}, i_1^{d*} = k_{PC}^P (q^* - q_{avg}) + k_{PC}^P \phi^\alpha, \text{ and } i_1^c = k_{PC}^P (p^* - p_{avg}) + k_{PC}^P \phi^\beta$.

### C. Proof of Corollary 1

We begin by noting that the PLL dynamics are decoupled, and the its parameters in the individual and reduced-order models are the same, therefore $\forall \ell \geq \ell_0, \ x_{PLL,\ell}(t) = x_{PLL,\ell}(t_0)$ $\forall \ell$ if we initialize $x_{PLL,\ell}(t_0) = x_{PLL,\ell}(t_0)$. Next, partition the permuted versions of (12) and (16), excluding the PLL dynamics, as

$$A_{LCL} = \begin{bmatrix} -\frac{R_i}{L_i} & 0 & 0 & 0 \\ \omega_{nom} \frac{R_i}{L_i} & -\omega_{nom} & 0 & 0 \\ 0 & 0 & -\frac{R_i}{L_i} & 0 \\ -\frac{R_i}{L_i} \frac{R_i}{L_i} & 0 & 0 & \omega_{nom} \frac{R_i}{L_i} \\ \end{bmatrix},$$

$$A_{PLL} = \begin{bmatrix} -\omega_{nom} & 0 & 0 & 0 \\ 0 & -\omega_{c,PLL} & 0 & 0 \\ 0 & 0 & -1 & 0 \\ \end{bmatrix},$$

$$B_{PLL} = \begin{bmatrix} 0 \\ 0 \\ \end{bmatrix},$$

$$B_{CCL} = \begin{bmatrix} 0 \frac{R_i}{L_i} \frac{R_i}{L_i} & 0 \end{bmatrix}^T, B_{PC} = \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \\ \end{bmatrix}.$$

Figure 6: Simulation results comparing the injected current for a system of 100 parallel-connected inverters with all inverter dynamics simulated superimposed to results from the reduced-order model for the following cases: (a) heterogeneous power ratings with power-scaling parameters $(\kappa)$ vary between 0.5 and 5, (b) identical power ratings with $\kappa = 1$ and LCL-filter parameters vary between $\pm 10\%$ of their nominal values, (c) same setup as (b), but with variation of $\pm 80\%$.  

### B. State-space Model Particulars
where $\tilde{g}_1 : \mathbb{R}^{16} \times \mathbb{R}^2 \times \mathbb{R}^2 \rightarrow \mathbb{R}^{10}$ and $\tilde{g}_2 : \mathbb{R}^{16} \times \mathbb{R}^2 \times \mathbb{R}^2 \rightarrow \mathbb{R}^2$ are the nonlinear parts of the dynamics of $\lambda_t$ and $v_{t,k}^{\alpha,\beta}$, respectively (similarly for $\tilde{g}_1^T$ and $\tilde{g}_2^T$). We bring to note a slight abuse of notation in terms of the submatrices in (45) and (46) and those in (22) and (23). Furthermore, the submatrices in (45) and (46) also follow the relationships in (24). Define $z_1 := \lambda_t - \sum_{\ell=1}^N \lambda_{t,\ell}$ and $z_2 := N v_{t,k}^{\alpha,\beta} - \sum_{\ell=1}^N v_{t,k}^{\alpha,\beta}$. The dynamics of $z_1$ and $z_2$ are:

$$
\dot{z}_1 = \lambda^T - \sum_{\ell=1}^N \lambda_{t,\ell} = \tilde{A}_{11} \lambda^T + \tilde{A}_{12} v_{t,k}^{\alpha,\beta} + \tilde{B}_{11} u_{t,k}^1 + \tilde{B}_{21} u_{t,k}^2
$$

and

$$
\dot{z}_2 = N v_{t,k}^{\alpha,\beta} - \sum_{\ell=1}^N \lambda_{t,\ell} = N \left( \tilde{A}_{21} \lambda^T + \tilde{A}_{22} v_{t,k}^{\alpha,\beta} + \tilde{B}_{22} u_{t,k}^1 \right) + \tilde{B}_{22} u_{t,k}^2 + \tilde{g}_1(\tilde{x}_{t,\ell}, u_{t,k}^1, u_{t,k}^2)
$$

Next, we will show that

$$
\tilde{g}_1(\tilde{x}_{t,k}, u_{t,k}^1, u_{t,k}^2) - \sum_{\ell=1}^N \tilde{g}_1(\tilde{x}_{t,\ell}, u_{t,k}^1, u_{t,k}^2) = \tilde{g}_1(\chi, 0, 2, u_{t,k}^2),
$$

$$
N \tilde{g}_2(\tilde{x}_{t,k}, u_{t,k}^1, u_{t,k}^2) - \sum_{\ell=1}^N \tilde{g}_2(\tilde{x}_{t,\ell}, u_{t,k}^1, u_{t,k}^2) = \tilde{g}_2(\chi, 0, 2, u_{t,k}^2),
$$

where $\chi := \left[ z_1^T, z_2^T, x_{t,k}^T, \phi_{t,k}^T \right]^T$. Let $\tilde{g}_1(\tilde{x}_{t,\ell}, \tilde{g}_2, g_{t,k}(\tilde{x}_{t,\ell}), g_{t,k}^T(\tilde{x}_{t,\ell})$, and $g_{t,k}^T(\tilde{x}_{t,\ell})$ denote the $k$-th entries of $\tilde{g}_1(\tilde{x}_{t,\ell}, u_{t,k}^1, u_{t,k}^2)$, $\tilde{g}_2(\tilde{x}_{t,\ell}, u_{t,k}^1, u_{t,k}^2)$, and $g_{t,k}^T(\tilde{x}_{t,\ell}, u_{t,k}^1, u_{t,k}^2)$, respectively.
These equalities are satisfied when the following identities (24), (49) and (50) hold. Using identities (24), (49) and (50), we have
\[ z_1 = A_{11} z_1 + A_{12} z_2 + \hat{g}_1 (\chi, 0_2, u_2), \]
(63)
\[ z_2 = A_{21} z_1 + A_{22} z_2 + \hat{g}_2 (\chi, 0_2, u_2). \]
(64)
If we initialize \( z_1 (t_0) = 0 \), and \( z_2 (t_0) = 0 \), we have \( z_1 (t) = 0_1, z_2 (t) = 0_2, \forall t \geq t_0 \) since \( g_1 (\chi, 0_2, u_2) = 0_1 \) and \( g_2 (\chi, 0_2, u_2) = 0_2 \) when \( z_1 = 0_1 \) and \( z_2 = 0_2 \). By the definition of \( z_1 \) and \( z_2 \), we have \( X^t = \sum_{t=1}^{N} \lambda_t (t), v_{\alpha\beta}^t (t) = \frac{1}{N} \sum_{t=1}^{N} v_{\alpha\beta}^t (t), \forall t \geq 0_1 \).

For the other direction, given that \( v_{\alpha\beta} \geq 0 \), \( z_1 (t) = X^t (t) = \sum_{t=1}^{N} \lambda_t (t), v_{\alpha\beta}^t (t) = \frac{1}{N} \sum_{t=1}^{N} v_{\alpha\beta}^t (t), \forall t \geq 0_1 \).

It is straightforward to see that (17a) and the unscaled parameters are the only set of parameters that satisfy (65). For the rest of the parameters, i.e., \( R_i, L_i, k^{CC}_{CC}, \) and \( k^{CC}_{PLL}, \) it can be derived that they always appear in (65)–(67) as fractions of \( \frac{R_i}{L_i}, \frac{k^{CC}_{PLL}}{L_i}, \) and \( \frac{k^{CC}_{PLL}}{L_i}. \) Therefore, they are related to those in the reduced-order model through (17b). This concludes the proof.

**References**


Miguel Rodriguez (S’06, M’11) was born in Gijon, Spain, in 1982. He received the M.S. and Ph.D. degrees in telecommunication engineering from the University of Oviedo, Oviedo, Spain, in 2006 and 2011, respectively. From 2011 to 2013, he was a Post-Doctoral Research Associate with the Colorado Power Electronics Center, University of Colorado at Boulder, Boulder, CO, USA. In 2013 he joined Advanced Micro Devices, Inc., in Fort Collins, CO, USA, as a member of the Low Power Advanced Development Group. His current research interests include dc/dc conversion and digital control, analysis of power delivery networks, and linear and switched integrated voltage regulators for microprocessors and SoCs.

Saber Jafarpour (M’16) is a Postdoctoral researcher with the Center for Control, Dynamical Systems and Computation at the University of California, Santa Barbara. He received his Ph.D. in 2016 from the Department of Mathematics and Statistics at Queen’s University. His research interests focus on network systems and synchronization of coupled oscillators with application to power grids.

Francesco Bullo (S’95, M’99, SM’03, F’10) is a Professor with the Mechanical Engineering Department and the Center for Control, Dynamical Systems and Computation at the University of California, Santa Barbara. He was previously associated with the University of Padova (Laurea degree in Electrical Engineering, 1994), the California Institute of Technology (Ph.D. degree in Control and Dynamical Systems, 1999), and the University of Illinois. His research interests focus on network systems and distributed control with application to robotic coordination, power grids and social networks. He is the coauthor of “Geometric Control of Mechanical Systems” (Springer, 2004) and “Distributed Control of Robotic Networks” (Princeton, 2009); his “Lectures on Network Systems” (CreateSpace, 2018) is available on his website. He is a Fellow of IEEE and IFAC. He has served on the editorial boards of IEEE, SIAM, and ESAIM journals, and serves as 2018 IEEE CSS President.

Victor Purba (S’15) received the Bachelor’s and M.S. degrees in electrical engineering in 2014 and 2016, respectively, from University of Minnesota, Minneapolis, MN, USA, where he is currently pursuing the Ph.D. degree in electrical engineering. His research interests include modeling, analysis, and control of power systems with the integration of renewable energy sources.

Brian Johnson (S’08, M’13) obtained the M.S. and Ph.D. degrees in Electrical and Computer Engineering from the University of Illinois at Urbana-Champaign, Urbana, in 2010 and 2013, respectively. He is the Washington Research Foundation Innovation Assistant Professor within the Department of Electrical and Computer Engineering at the University of Washington. Prior to joining the University of Washington in 2018, he was an engineer with the National Renewable Energy Laboratory in Golden, CO. He was awarded a National Science Foundation Graduate Research Fellowship in 2010, and currently serves as an Associate Editor for the IEEE Transactions on Energy Conversion. His research interests are in renewable energy systems, power electronics, and control systems.

Sairaj V. Dhople (M’13) received the B.S., M.S., and Ph.D. degrees in electrical engineering from the University of Illinois at Urbana-Champaign, Urbana, IL, USA, in 2007, 2009, and 2012, respectively. He is currently an Associate Professor with the Department of Electrical and Computer Engineering, University of Minnesota, Minneapolis, MN, USA, where he is with the Power and Energy Systems research group. His research interests include modeling, analysis, and control of power electronics and power systems with a focus on renewable integration. Dr. Dhople received the National Science Foundation CAREER Award in 2015. He is an Associate Editor for the IEEE Transactions on Energy Conversion and the IEEE Transactions on Power Systems.